



10/658742

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PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re U.S. Patent No. 6,906,569 *B2*

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Serial No. 10/658,742

Inventor(s): Martin BROX

Filed: September 10, 2003

Issue Date: June 14, 2005

Attorney Docket No. 006410.00001

For: DIGITAL SIGNAL DELAY DEVICE

**REQUEST FOR CERTIFICATE OF CORRECTION**

U.S. Patent and Trademark Office  
Customer Service Window  
Randolph Building, Mail Stop: Certificate of Correction Branch  
401 Dulany Street  
Alexandria, VA 22314

**Certificate**  
**FEB 14 2006**  
**of Correction**

Sir:

Pursuant to 35 U.S.C. § 254 and 37 C.F.R. § 1.322, this is a request for the issuance of a Certificate of Correction in the above-identified patent. Two (2) copies of PTO Form 1050 are appended. The complete Certificate of Correction involves one page.

The mistakes identified in the appended Form occurred through no fault of the Applicant, as clearly disclosed by the records of the application, which matured into this patent. Enclosed for your convenience is the relevant portion of the Amendment filed January 5, 2005.

Issuance of the Certificate of Correction containing the corrections is respectfully requested. Since these changes are necessitated through no fault of the Applicant, no fee is believed to be associated with this request. Nonetheless, should the Patent and Trademark Office determine that a fee is required, please charge our Deposit Account No. 19-0733.

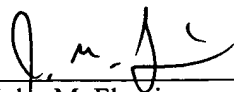
Respectfully submitted,

BANNER & WITCOFF, LTD.

Dated: February 9, 2006

1001 G Street, N.W. (11th Fl.)  
Washington, D.C. 20001  
(202) 824-3000

By: \_\_\_\_\_

  
John M. Fleming  
Registration No. 56,536

**FEB 15 2006**

UNITED STATES PATENT AND TRADEMARK OFFICE  
**CERTIFICATE OF CORRECTION**

PATENT NO.: 6,906,569 *B2*

DATED: June 14, 2005

INVENTOR(S): Martin BROX

It is certified that errors appear in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

In Column 11, Claim 11, Line 47:

Please replace "(N)," with --(IN),--

In Column 12, Claim 14, Line 11:

Please replace "claim 1" with --claim 13--

Mailing Address of Sender:

Banner & Witcoff, Ltd.  
11th Floor  
1001 G Street, N.W.  
Washington, DC 20001-4597

FORM PTO 1050 (Rev.2-93)

U.S. PAT. NO 6,906,569

No. of add'l copies  
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☐

FEB 15 2006

UNITED STATES PATENT AND TRADEMARK OFFICE  
**CERTIFICATE OF CORRECTION**

PATENT NO.: 6,906,569 *B2*

DATED: June 14, 2005

INVENTOR(S): Martin BROX

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In Column 11, Claim 11, Line 47:

Please replace "(N)," with --(IN),--

In Column 12, Claim 14, Line 11:

Please replace "claim 1" with --claim 13--

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FORM PTO 1050 (Rev.2-93)

**FEB 15 2006**



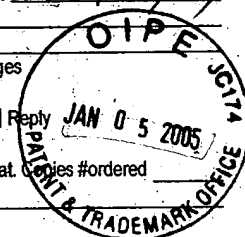
☒ PATENT ☐ DESIGN B&W Ref. 006410.0001 Date 1/5/05  
☐ HAND CARRY Group                      Bldg                      Rm                       
Serial/Patent No. 10/658,742 Atty/Sec: JM/FLL  
Inventor: Bruce Client: Infidion Technologies  
Title: Digital Signal Delay

The following has been received in the U.S. Patent and Trademark Office on the date stamped hereon:

- |   |   |
|---|---|
| <input type="checkbox"/> total pp Spec., including: # of Claims   | <input type="checkbox"/> Request for Corrected: <input type="checkbox"/> Filing Receipt <input type="checkbox"/> Assignment                         |
| <input type="checkbox"/> # of independent claims  | <input type="checkbox"/> Response to Restriction/Election Requirement   |
| <input type="checkbox"/> Application Data Sheet (ADS): <input type="checkbox"/> Initial <input type="checkbox"/> Supplemental | <input type="checkbox"/> Sequence Listing: <input type="checkbox"/> Diskette <input type="checkbox"/> Paper <u>                    </u> pages       |
| <input type="checkbox"/> Drawings: <input type="checkbox"/> Formal <input type="checkbox"/> Informal                          | <input type="checkbox"/> Amendment <u>Response</u> OA <u>10/5/04</u>  |
| <input type="checkbox"/> # of distinct sheets   | <input type="checkbox"/> Petition for Extension of Time until <u>                    </u>   |
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| <input type="checkbox"/> Assignment w/PTO Cover Sheet   | <input type="checkbox"/> Notice of Appeal & Fee   |
| <input type="checkbox"/> IDS w/PTO 1449 <input type="checkbox"/> References <input type="checkbox"/> w/Fee                    | <input type="checkbox"/> Brief: <input type="checkbox"/> Appeal & Fee <input type="checkbox"/> Reply <u>JAN 05 2005</u>                             |
| <input type="checkbox"/> Preliminary Amendment  | <input type="checkbox"/> Request for Oral Hearing   |
| <input type="checkbox"/> Priority Claim: (Foreign or U.S. Provisional) B&W# <u>                    </u>                       | <input type="checkbox"/> Issue Fee <input type="checkbox"/> Pub. Fee <input type="checkbox"/> Adv. Pat. Copies #ordered <u>                    </u> |

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|--|---|
| Country <u>                    </u> Appl. # <u>                    </u> Date <u>                    </u>                                     | <input type="checkbox"/> Notice of Allowance dtd <u>                    </u>  |
| <input type="checkbox"/> w/Foreign Priority Document(s)  | <input type="checkbox"/> Amendment under 37 CFR 1.312   |
| <input type="checkbox"/> Application: <input type="checkbox"/> CIP <input type="checkbox"/> Continuation <input type="checkbox"/> Divisional | <input type="checkbox"/> Request for Certificate of Correction  |
| Parent SN: <u>                    </u> B&W# <u>                    </u>  | <input checked="" type="checkbox"/> Transmittal <input type="checkbox"/> Fee Transmittal w/Auth. to Charge Deposit Acct |
| <input type="checkbox"/> U.S. Provisional <u>                    </u> pp Spec/Claims; Cover Sheet  | <input type="checkbox"/> Certificate of Mailing   |
| <input type="checkbox"/> Response to Missing Parts/Requirements dtd <u>                    </u>  | <input type="checkbox"/> Check No. <u>                    </u> for \$ <u>                    </u>                       |
| <input type="checkbox"/> Response to Notice to File Corrected Appln. Papers dtd <u>                    </u>                                  | <input checked="" type="checkbox"/> Replacement Sheet Fig 1   |
| <input type="checkbox"/> Request for Expedited Foreign Filing License  | <input type="checkbox"/> Fig 3+4 (2 sheets)   |

☒ Annotated Sheet for Figs 1, 3+4 (2 sheets) (25)



B&W Rev. 01/04

FEB 15 2006

Mail Date Canceled

Appl. No. 10/658,742  
Amdt. Dated January 5, 2005  
Reply to Non-Final Office Action of October 5, 2004



Appl. No. : ~~10/658,742~~  
Applicant : Martin Brox  
Filed : September 10, 2003  
Title : Digital Signal Delay Device

TC/A.U. : 2816  
Examiner : An T. Luu

Docket No. : 006410.00001  
Confirmation No. : 3389

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

### **RESPONSE AND AMENDMENT**

Sir:

In response to the non-final Office Action mailed October 5, 2004, please amend the instant application as follows:

**Amendments to the Specification** begin on page 2 of this paper.

**Amendments to the Claims** are reflected in the listing of claims which begins on page 3 of this paper.

**Amendments to the Drawings** begin on page 8 of this paper and include both an attached replacement sheet and an annotated sheet showing changes.

**Remarks/Arguments** begin on page 9 of this paper.

An **Appendix** including amended drawing figures is attached following page 13 of this paper.

Claim 9 (currently amended): The digital signal delay device according to claim 826, wherein the gates, depending on whether ~~they~~ the gates are connected with a signal delay element whose output signal is inverted or non-inverted vis-à-vis the signal ~~(IN)~~ [-] comprise an inverter circuit arrangement or a transfer gate circuit arrangement.

Claim 10 (currently amended): The digital signal delay device according to claim 826, wherein the gates, depending on whether ~~they~~ the gates are connected with a signal delay element whose output signal is inverted or non-inverted vis-à-vis the signal (IN), [-] comprise two inverter circuit arrangements, or a transfer gate circuit arrangement and an inverter circuit arrangement.

Claim 11 (currently amended): The digital signal delay device according to claim 826, wherein the gates, depending on whether ~~they~~ the gates are connected with a signal delay element whose output signal is inverted or non-inverted vis-à-vis the signal (IN), [-] comprise two inverter circuit arrangements and a transfer gate circuit arrangement, or three inverter circuit arrangements.

Claim 12 (original): The digital signal delay device according to ~~claim 11~~, wherein at least one of the inverter circuit arrangements is a tristate inverter circuit arrangement.

Claims 13-20 (canceled)

Claim 21 (new): The digital signal delay device according to claim 1, wherein the gates, depending on whether the gates are connected with a signal delay element whose output signal is inverted or non-inverted vis-à-vis the signal (IN), comprise an inverter circuit arrangement or a transfer gate circuit arrangement.

Claim 22 (new): The digital signal delay device according to claim 1, wherein the gates, depending on whether the gates are connected with a signal delay element whose output signal is